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Date: <u>January 9, 2001</u>



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By:	Male	ors.
	Marc A. Rossi	

ASSISTANT COMMISSIONER FOR PATENTS

Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s):

Kazuo MATSUZAKI of Nagano, Japan;

Naoto FUJISHIMA of Nagano, Japan; Akio KITAMURA of Nagano, Japan;

Gen TADA of Nagano, Japan; Masaru SAITO of Nagano, Japan.

For:

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A SEMICONDUCTOR DEVICE EXHIBITING A HIGH BREAKDOWN VOLTAGE AND THE METHOD OF MANUFACTURING THE SAME

Enclosed are:

- [X] THIRTEEN (13) sheets of informal drawings, Figures 1-24.
- [] An Assignment of the invention to Fuji Electric Co., Ltd.
- [X] The application claims priority from Japanese Applications: 2000-008961 filed January 18, 2000; 2000-146704 filed May 18, 2000.
- [X] Certified copies of the priority documents are enclosed.

- [] A Declaration and Power of Attorney.
- [] A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.
- [] A Preliminary Amendment is enclosed.
- [X] An Information Disclosure Statement.

	Number	Number Number		Basic			
	Filed	Extra		Rate	Fee	\$710.00	
Total							
Claims	28 - 20	= 8	X	\$ 18.00	==	\$ 144.00	
Independent							
Claims	9 - 3	= 6	X	\$ 80.00	=	480.00	
Multiple Dependent Claim(s)							
(if applicable	e)		+	\$270.00	=	0.00	
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				Total		\$1334.00	
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2	1011211211					\$1334.00	

- Our Check No. _____ in the amount of the total filing fee is enclosed. However, if the check is missing or insufficient, the Commissioner is authorized to charge any additional fees which may be required, or credit any overpayment, to our Deposit Account No. 18-2056.
- [] The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to our Deposit Account No. 18-2056: any patent application processing fees under 37 CFR 1.17; and any filing fees under 37 CFR 1.16 for presentation of extra claims.

Marc A Rossi

Registration No. 31,923